P1

Pin		Exposed Functions				STM32 Pin	P1 Pin #	P1 Pin Name
VIN	VIN							
GND	GND							
TX		USART1_TX	TIM1_CH2			PA9	64	MICRO_UART1_TXD
RX		USART1_RX	TIM1_CH3			PA10	63	MICRO_UART1_RXD
WKP	ADC0		TIM5_CH1			PA0	30	MICRO_UART2_CTS
DAC	ADC4			DAC1		PA4	24	MICRO_SPI1_SS [2]
A5	ADC7	SPI (MOSI)	TIM3_CH2			PA7	23	MICRO_SPI1_MOSI
A4	ADC6	SPI (MISO)	TIM3_CH1			PA6	21	MICRO_SPI1_MISO
A3	ADC5	SPI (SCK)		DAC2		PA5	22	MICRO_SPI1_SCK
A2	ADC12	SPI (SS)				PC2	49	MICRO_GPIO_13
A1	ADC13					PC3	43	MICRO_GPIO_5
A0	ADC15					PC5	50	MICRO_GPIO_14

P1

Pin		Exposed Functions				STM32 Pin	P1 Pin #	P1 Pin Name
3V3	3V3							
RST	RST					E8	34	MICRO_RST_N
VBAT	VBAT					A9	38	VBAT
GND	GND							
D7	JTAG_TMS					PA13	54	MICRO_JTAG_TMS
D6	JTAG_TCK					PA14	55	MICRO_JTAG_TCK
D5	JTAG_TDI	SPI1 (SS)			I2S3_WS	PA15	53	MICRO_JTAG_TDI
D4	JTAG_TDO	SPI1 (SCK)			I2S3_SCK	PB3	52	MICRO_JTAG_TDO
D3	JTAG_TRST	SPI1 (MISO)		TIM3_CH1		PB4	51	MICRO_JTAG_TRSTN
D2		SPI1 (MOSI)	CAN2_RX	TIM3_CH2	I2S3_SD	PB5	45	MICRO_GPIO_7
D1	SCL		CAN2_TX	TIM4_CH1		PB6	35	MICRO_I2C1_SCL
D0	SDA			TIM4_CH2		PB7	36	MICRO_I2C1_SDA

P1

User I/O	Exposed Functions				STM32 Pin	P1 Pin #	P1 Pin Name
RGB LED - RED			TIM2_CH2		PA1	29	MICRO_UART2_RTS
RGB LED - GREEN			TIM2_CH3		PA2	32	MICRO_UART2_TXD
RGB LED - BLUE			TIM2_CH4		PA3	31	MICRO_UART2_RXD
Setup Button			TIM3_CH2	I2S3_MCK	PC7	46	MICRO_GPIO_8
Reset Button					E8	34	MICRO_RST_N
USB Data+					PA12	61	MICRO_UART1_RTS
USB Data-					PA11	62	MICRO_UART1_CTS
P1S0		ADC8	TIM3_CH3		PB0	40	MICRO_GPIO_1
P1S1		ADC9	TIM3_CH4		PB1	41	MICRO_GPIO_2
P1S2		ADC10			PC0	42	MICRO_GPIO_3
P1S3		ADC14			PC4	44	MICRO_GPIO_6
P1S4					PC13	47	MICRO_GPIO_9
P1S5		ADC11			PC1	48	MICRO_GPIO_12
P1S6 [1]			TIM1_CH1		PA8	33	TESTMODE

Peripheral Key

ADC	SPI	PWM/Servo/Tone	
JTAG	SPI1	I2S	DAC
I2C/Wire	Serial1	CAN	

Notes:

[1] Connected to MCO1 by default, outputs 32kHz clock for WICED powersave mode. See System Features in the Firmware Reference to disable the Wi-Fi Powersave Clock and allow usage of this pin.

[2] MICRO_SPI1_SS is only for reference as a P1 module pin name. It is technically speaking the STM32 pin PA4 which is the SS pin in an hardware SPI driven sense, however in the Particle API SPI SS is only user controlled as a GPIO. The hardware SS pin is not implemented. The default SS pin for the Particle SPI API is A2 (STM32 pin PC2), but any GPIO can be used for this function with SPI.begin(pin).